Agilent Technologies, Inc.
Attorney Docket No. 10021074-1
Title: "Folded Flex Circuit Interconnect having a Grid Array Interface"
Inventor(s): Steven A. Rosenau et al.

1/8

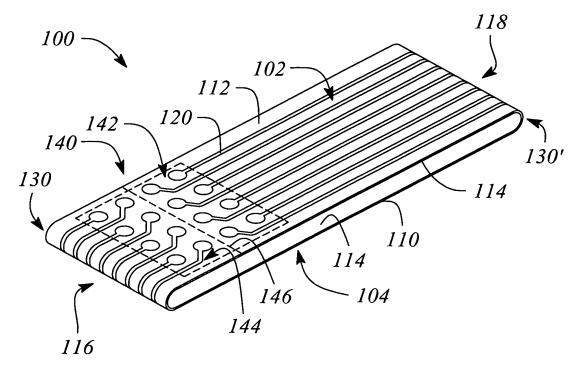


FIG. 1A

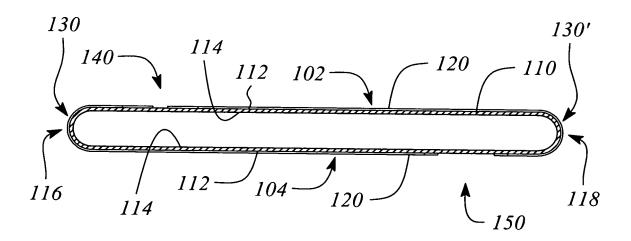


FIG. 1B



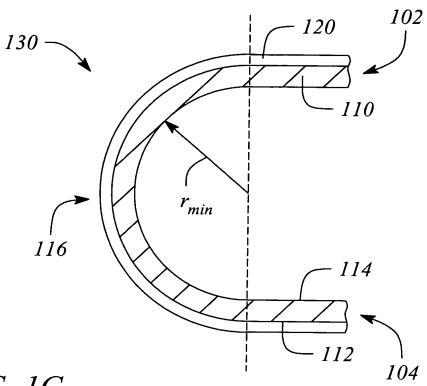


FIG. 1C

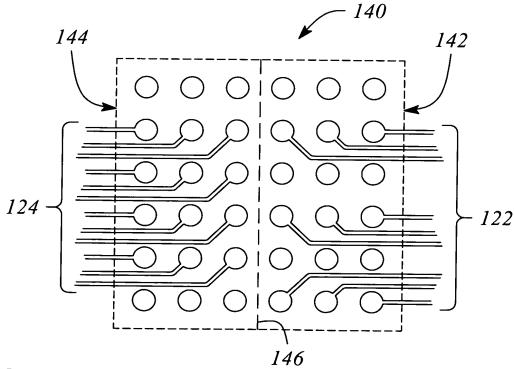


FIG. 1D

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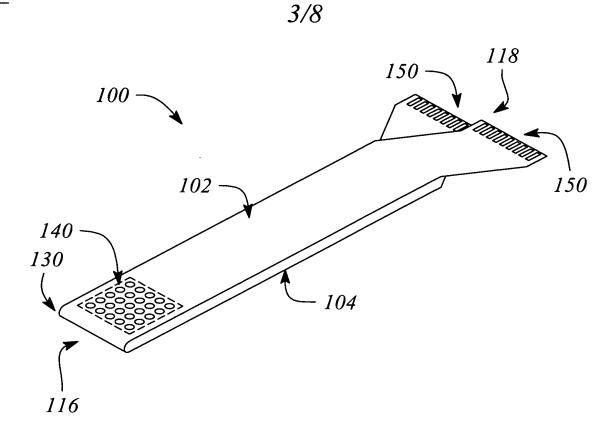


FIG. 2A

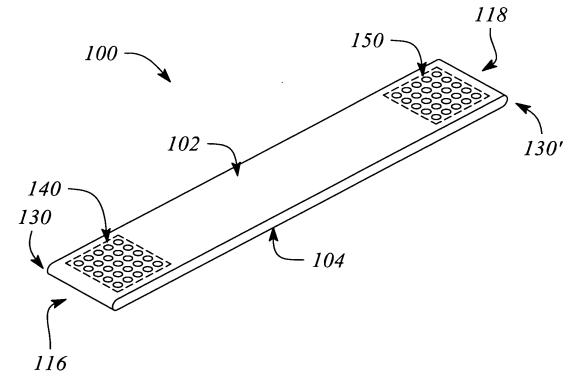
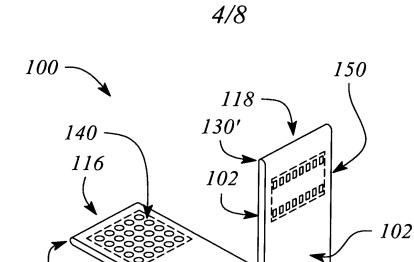


FIG. 2B

## Agilent Technologies, Inc. Attorney Docket No. 10021074-1

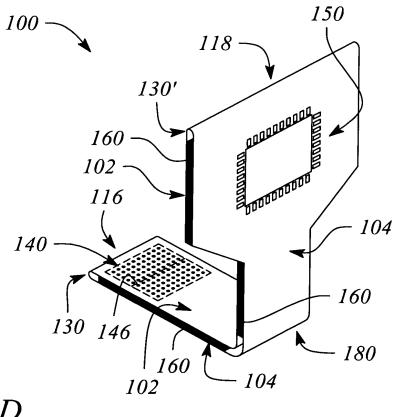
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102

FIG. 2C

130



180

104

FIG. 2D

## Agilent Technologies, Inc. Attorney Docket No. 10021074-1 Title: "Folded Flex Circuit Interconnect having a Grid Array Interface" Inventor(s): Steven A. Rosenau et al.

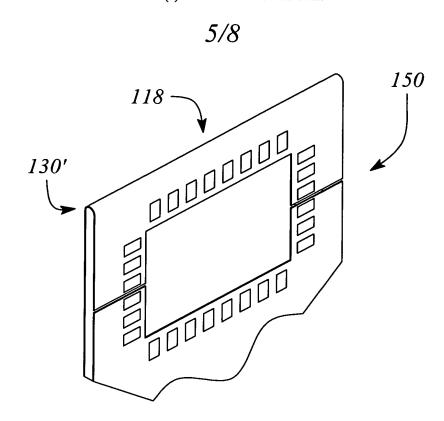


FIG. 3A

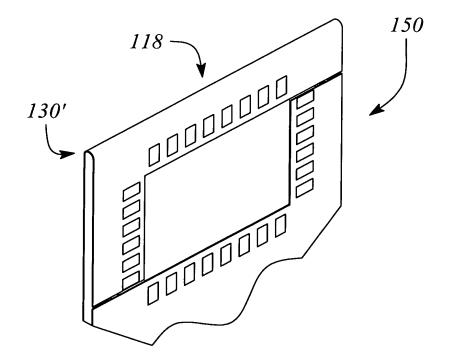
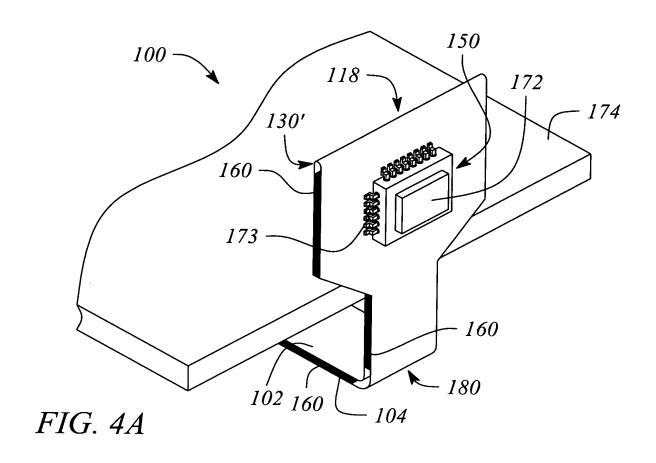


FIG. 3B

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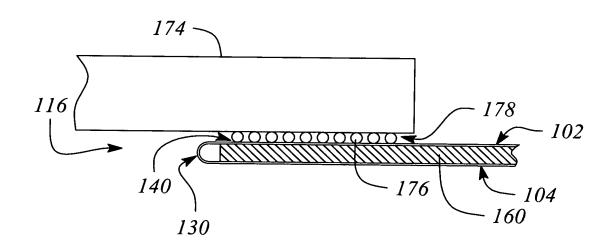


FIG. 4B

7/8

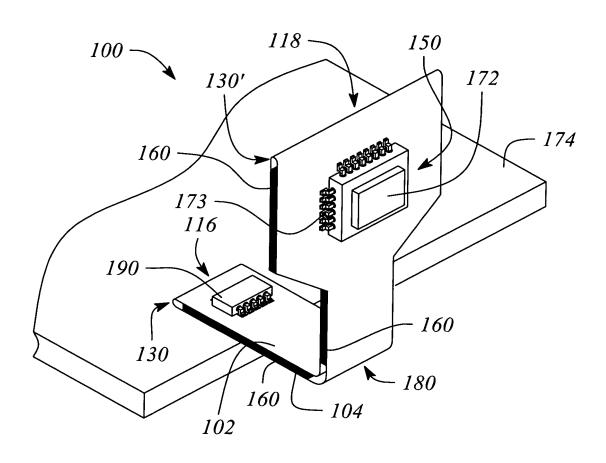


FIG. 4C

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